

AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph on page 12, lines 15-20 as follows.

Figure 3 illustrates a method 900 of modeling a complex microcontroller. At the step 902, the CPU 814 of the hardware model 804 is put into an effectively inactive state. At the step 904, the memory access from the ISS 400 to device registers in peripheral devices 208 834 is serviced. At the step 906, the direct memory access (DMA) 837 cycles generated by peripheral devices 834 are serviced. At the step 908, the interrupt requests generated by peripheral devices 834 are serviced.